



**PATENT**  
5957-82403

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor: Katsuki Hazama

Serial Number: 10/725,318

Filing Date: December 2, 2003

Title: Semiconductor Device Including  
Transistor with Composite Gate  
Structure and Transistor with Single  
Gate Structure, and Method for  
Manufacturing the Same

Atty.Dkt.No.: 5957-82403

Examiner: Richard A. Booth

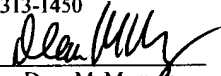
Group/Art Unit: 2812

**CERTIFICATE OF FIRST CLASS MAIL  
UNDER 37 C.F.R. 1.8**

I hereby certify that this Correspondence is being deposited  
with the United States Postal Service with sufficient postage  
for first class mail in an envelope addressed to:

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

On: December 26, 2006  
Date

  
Dean M. Munyon

**RESPONSE TO OFFICE ACTION MAILED SEPTEMBER 25, 2006**

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to an Office Action of September 25, 2006, to further highlight why the application is in condition for allowance.

Please amend the application as listed below.